

AMENDMENTS TO THE CLAIMS

Please amend Claims 1, 29, 67, and 95 as follows:

Please cancel Claims 10-14, 36-66, 76-80, and 102-132 without prejudice to their prosecution in a divisional patent application.

1. (**Currently amended**) An apparatus for receiving an audio signal via a powerline network, the apparatus comprising:

a housing;

a powerline module located in the housing and configured to convert a powerline signal received via a powerline network into a combined signal;

a receiver module located in the housing and configured to ~~receive a combined signal via a network and~~ extract a control signal and an audio signal from the converted combined signal, the receiver module manipulating the audio signal based on the extracted control signal;

a plug coupled to the housing and configured for insertion into an electrical receptacle;

a power supply in the housing, coupled to the plug and configured to distribute electrical energy to the receiver module; and

an output wire configured to couple the housing to an output device.

2. (Original) The apparatus of claim 1, wherein the housing further incorporates an address switch configured for selecting an address from a plurality of addresses.

3. (Original) The apparatus of claim 2, wherein the housing further incorporates a power switch configured to select an off state or on state for the receiver module.

4. (Original) The apparatus of claim 3, wherein the housing further incorporates an amplifier configured to amplify the audio signal based in part upon the control signal, wherein the power supply is further configured to provide power to the amplifier.

5. (Original) The apparatus of claim 4, wherein the housing further incorporates a light emitting diode power indicator configured to emit light when the power supply is providing electrical energy to the receiver module.

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6. (Original) The apparatus of claim 5, wherein the housing further incorporates a light emitting diode receiver indicator configured to emit light when the receiver module is receiving the combined signal.

7. (Original) The apparatus of Claim 6, wherein the housing further incorporates a Digital Signal Processor (DSP) module configured to manipulate the audio signal based on the extracted control signal.

8. (Original) The apparatus of Claim 7, wherein the amplifier is a digital amplifier configured to digitally amplify the audio signal.

9. (Original) The apparatus of Claim 7, wherein the combined signal includes an address signal which is associated with the output device.

10. (Cancelled)

11. (Cancelled)

12. (Cancelled)

13. (Cancelled)

14. (Cancelled)

15. (Original) The apparatus of Claim 2, wherein the control signal is analog.

16. (Original) The apparatus of Claim 2, wherein the audio signal is digital.

17. (Original) The apparatus of Claim 2, wherein the control signal is digital.

18. (Original) The apparatus of Claim 2, wherein the control signal is a volume level.

19. (Original) The apparatus of Claim 2, wherein the control signal is a balance level.

20. (Original) The apparatus of Claim 2, wherein the control signal is a fader level.

21. (Original) The apparatus of Claim 2, wherein the control signal is a sub-bass level.

22. (Original) The apparatus of Claim 2, wherein the control signal is a destination source.

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23. (Original) The apparatus of Claim 2, wherein the control signal is a sound processing selection.

24. (Original) The apparatus of Claim 2, wherein the control signal is an equalizer level.

25. (Original) The apparatus of Claim 2, wherein the control signal is an address.

26. (Original) The apparatus of Claim 2, wherein the control signal is a power on.

27. (Original) The apparatus of Claim 2, wherein the control signal is a power off.

28. (Original) The apparatus of Claim 2, wherein the control signal is a time delay.

29. (**Currently amended**) The apparatus of Claim 2, wherein the control signal is a phase delay associated with the audio signal.

30. (Original) The apparatus of Claim 2, wherein the receiver module is configured to power on in response to receiving the combined signal.

31. (Original) The apparatus of Claim 2, wherein the receiver module is configured to power off in response to not receiving the combined signal.

32. (Original) The apparatus of Claim 2, wherein the control signal is in an I²C format.

33. (Original) The apparatus of Claim 2, wherein the audio signal is in an inter IC sound (I²S) format.

34. (Original) The apparatus of Claim 2, wherein the output device is a loudspeaker.

35. (Original) The apparatus of Claim 2, wherein the output device is a headphone.

36. (Cancelled)

37. (Cancelled)

38. (Cancelled)

39. (Cancelled)

40. (Cancelled)

41. (Cancelled)

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42. (Cancelled)

43. (Cancelled)

44. (Cancelled)

45. (Cancelled)

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61. (Cancelled)

62. (Cancelled)

63. (Cancelled)

64. (Cancelled)

65. (Cancelled)

66. (Cancelled)

67. (**Currently amended**) An apparatus for receiving an audio signal via a powerline network, the apparatus comprising:
a first housing comprising,

a powerline module configured to convert a powerline signal received via a powerline network into a combined signal;

a receiver module configured to ~~receive a combined signal via a network~~ and extract a control signal and an audio signal from the converted combined signal, the receiver module manipulating the audio signal based on the extracted control signal;

a second housing comprising,

a plug configured for insertion into an electrical receptacle,

a power supply coupled to the plug and configured to distribute electrical energy to the receiver module;

a wire coupled between the first housing and the second housing; and

an output wire configured to couple the first housing to an output device.

68. (Original) The apparatus of claim 67, further comprising an address switch configured for selecting an address from a plurality of addresses.

69. (Original) The apparatus of claim 68, further comprising a power switch configured to select an off state or on state for the receiver module.

70. (Original) The apparatus of claim 69, further comprising an amplifier configured to amplify the audio signal based in part upon the control signal, wherein the power supply is further configured to provide power to the amplifier.

71. (Original) The apparatus of claim 70, further comprising a light emitting diode power indicator configured to emit light when the power supply is providing electrical energy to the receiver module.

72. (Original) The apparatus of claim 71, further comprising a light emitting diode receiver indicator configured to emit light when the receiver module is receiving the combined signal.

73. (Original) The apparatus of Claim 72, further comprising a Digital Signal Processor (DSP) module configured to manipulate the audio signal based on the extracted control signal.

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74. (Original) The apparatus of Claim 73, wherein the amplifier is a digital amplifier configured to digitally amplify the audio signal.

75. (Original) The apparatus of Claim 73, wherein the combined signal includes an address signal which is associated with the output device.

76. (Cancelled)

77. (Cancelled)

78. (Cancelled)

79. (Cancelled)

80. (Cancelled)

81. (Original) The apparatus of Claim 68, wherein the control signal is analog.

82. (Original) The apparatus of Claim 68, wherein the audio signal is digital.

83. (Original) The apparatus of Claim 68, wherein the control signal is digital.

84. (Original) The apparatus of Claim 68, wherein the control signal is a volume level.

85. (Original) The apparatus of Claim 68, wherein the control signal is a balance level.

86. (Original) The apparatus of Claim 68, wherein the control signal is a fader level.

87. (Original) The apparatus of Claim 68, wherein the control signal is a sub-bass level.

88. (Original) The apparatus of Claim 68, wherein the control signal is a destination source.

89. (Original) The apparatus of Claim 68, wherein the control signal is a sound processing selection.

90. (Original) The apparatus of Claim 68, wherein the control signal is an equalizer level.

91. (Original) The apparatus of Claim 68, wherein the control signal is an address.

92. (Original) The apparatus of Claim 68, wherein the control signal is a power on.

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93. (Original) The apparatus of Claim 68, wherein the control signal is a power off.

94. (Original) The apparatus of Claim 68, wherein the control signal is a time delay.

95. (**Currently amended**) The apparatus of Claim 68, wherein the control signal is a phase delay associated with the audio signal.

96. (Original) The apparatus of Claim 68, wherein the receiver module is configured to power on in response to receiving the combined signal.

97. (Original) The apparatus of Claim 68, wherein the receiver module is configured to power off in response to not receiving the combined signal.

98. (Original) The apparatus of Claim 68, wherein the control signal is in an I²C format.

99. (Original) The apparatus of Claim 68, wherein the audio signal is in an inter IC sound (I2S) format.

100. (Original) The apparatus of Claim 68, wherein the output device is a loudspeaker.

101. (Original) The apparatus of Claim 68, wherein the output device is a headphone.

102-132. (Cancelled)